## Remarks

Applicants thank the Examiner for the careful examination of this application and the clear explanation of the rejections.

The undersigned attorney earlier asked the inventor Gene Frantz for additional references qualifying as prior art. Mr. Frantz replied that he knew of no additional prior art. The undersigned attorney also knows of no additional prior art.

Applicants submit a separate Information Disclosure Statement A, B, C, and D with Forms 1449.

The amended title conforms to the claimed subjects matter.

The new claims particularly point out and distinctly claim the subject matter the applicant regards as his invention.

The additional limitations of the new claims find support in the matter depicted in Figure 4 and described in the related specification text.

The independent claims 20 and 27 are allowable for comprising substantially the claimed matter of US 5,680,369, claim 1 and including, according to the specific wording of each, a command decoder coupled to address registers.

In particular, in claim 20, first and second address registers are coupled to the command decoder, and the device includes an address multiplexer and a control strobe terminal coupled to the command decoder.

In claim 27, first and second address registers are coupled to the command decoder, and the device includes an address multiplexer, a control strobe terminal coupled to the command decoder, and the data port includes a first write serial latch and a second write latch.

The independent claims avoid an obviousness-type double patenting rejection by including the command decoder and the first and second address latches that are not taught or suggested by the claims in US 5,680,369.

The cited art, in contrast to the claimed inventions, fails to teach or suggest a command decoder and address registers arranged as specifically required by the independent claims.

The depending claims provide additional limitations to indicate that the independent claims are of different and greater scope than the depending claims. For example, some of the depending claims, such as claims 24 and 31, are specific to the embodiment depicted in Figure 4, while the independent claims are believed to be broad enough to encompass present day synchronous dynamic random access memory devices, including DDR memory devices.

The depending claims also are allowable for including further details not taught or suggested by the cited art. For example, claims 26, and 33 require the control inputs of the command decoder to be coupled to the address terminals.

The application is in allowable form and the claims distinguish over the cited references. Applicants respectfully request reconsideration or further examination of this application.

Respectfully Submitted

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